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1 Poster session: Parallelizing the buckshot algorithm for efficient   **100%**
document clustering
Eric C. Jensen , Steven M. Beitzel , Angelo J. Pilotto , Nazli Goharian ,
Ophir Frieder
Proceedings of the eleventh international conference on Information
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> We present a parallel implementation of the Buckshot document
> clustering algorithm. We demonstrate that this parallel approach is
> highly efficient both in terms of load balancing and minimization of
> communication. In a series of experiments using the 2GB of SGML
> data from TReC disks 4 and 5, our parallel approach was shown to
> be scalable in terms of processors efficiently used and the number
> of clusters created.

2 Session S8.2: system synthesis: Instruction generation and   **100%**
regularity extraction for reconfigurable processors
Philip Brisk , Adam Kaplan , Ryan Kastner , Majid Sarrafzadeh
Proceedings of the international conference on Compilers, architecture,
and synthesis for embedded systems October 2002
> The increasing demand for complex and specialized embedded
> hardware must be met by processors which are optimized for
> performance, yet are also extremely flexible. In our work, we
> explore the tradeoff between flexibility and performance in the
> domain of reconfigurable processor design. Specifically, we seek to

identify regularly occurring, computation-heavy patterns in an application or set of applications. These patterns become candidates for hard-logic implementation, potentially embedded in t ...

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Jason Cong , M'Lissa Smith
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Proceedings of the 1995 IEEE/ACM international conference on
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Roman Kužnar , Franc Brglez , Baldomir Zajc
Proceedings of the 31st annual conference on Design automation
conference June 1994

8 Physical Design: Efficient circuit clustering for area and power   99%
reduction in FPGAs
Amit Singh , Malgorzata Marek-Sadowska
Tenth ACM International Symposium on Field-Programmable Gate
Arrays February 2002
We present a routability-driven bottom-up clustering technique for area and power reduction in clustered FPGAs. This technique uses a cell connectivity metric to identify seeds for efficient clustering. Effective seed selection, coupled with an interconnect-resource aware clustering and placement, can have a favorable impact on

circuit routability. It leads to better device utilization, savings in area, and reduction in power consumption. Routing area reduction of 35% is achieved over previously ...

9 Hierarchial clasterization, decomposition and multilevel macromodeling—the effective and efficient tools to solve the sigh and very high size combinatorial circuit type problems (abstract only) 99%
Roman Bazylevych
Proceedings of the symposium on Contemporary computing in Ukraine
February 2000

The paper gives a generalization of author and his scientific group recent works in combinatorial non-polynomial high and very high size problems that appear in physical design automation of electronic devices including VLSI and SoC. The optimal circuit reduction method is marked as the better tool to recognize the hierarchical cluster structure of the circuit. Its possibilities to solve the wide spectrum of various problems, including forced hierarchical partitioning with given constraints, ...

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Evan Speight , Hazim Abdel-Shafi , John K. Bennett
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Edward Omiecinski , Peter Scheuermann
ACM Transactions on Database Systems (TODS) December 1990
Volume 15 Issue 4
> We present an efficient heuristic algorithm for record clustering that
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> associated numbering scheme, which in the split phase allows each
> processor independently to compute the unique cluster number of a
> record satisfying an arbitrary query. We show that by restricting
> ourselves in the merge phase to combining only sibling clusters, we
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> number of processors used. Final ...

17 Data placement in Bubba   98%
George Copeland , William Alexander , Ellen Boughter , Tom Keller
Proceedings of the 1988 ACM SIGMOD international conference on
Management of data June 1988
> This paper examines the problem of data placement in Bubba, a
> highly-parallel system for data-intensive applications being
> developed at MCC. “Highly-parallel” implies that load
> balancing is a critical performance issue.
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ACM SIGMOD Record , Proceedings of the 1991 ACM SIGMOD
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C. Faloutsos , S. Roseman
Proceedings of the eighth ACM SIGACT-SIGMOD-SIGART symposium
on Principles of database systems March 1989
> In this paper we propose the use of fractals and especially the
> Hilbert curve, in order to design good distance-preserving
> mappings. Such mappings improve the performance of
> secondary-key- and spatial- access methods, where
> multi-dimensional points have to be stored on an 1-dimensional
> medium (e.g., disk). Good clustering reduces the number of disk
> accesses on retrieval, improving the response time. Our
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Proceedings of the 23rd ACM/IEEE conference on Design automation
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Saurabh N. Adya , Igor L. Markov
Proceedings of 2002 International Symposium on Physical Design April 2002

> While a number of recent works address large-scale standard-cell placement, they typically assume that all macros are fixed. Floorplanning techniques are very good at handling macros, but do not scale to hundreds of thousands of placeable objects. Therefore we

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